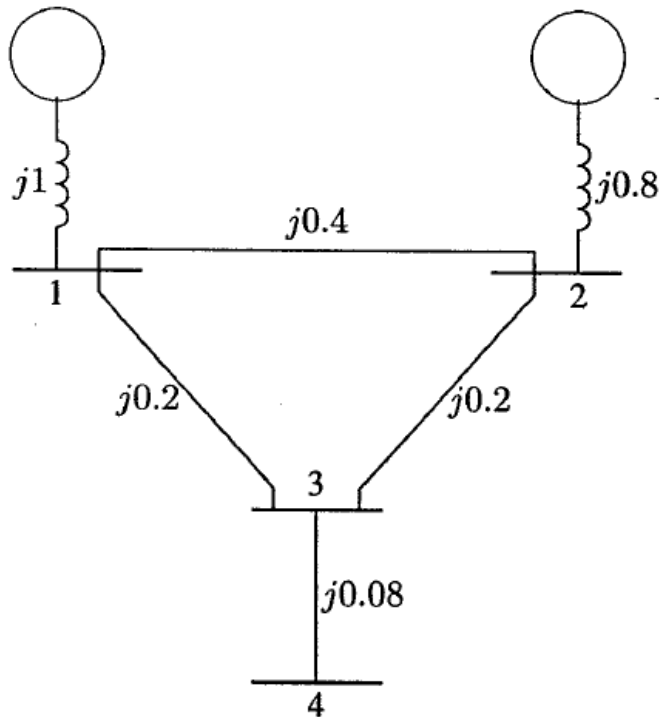
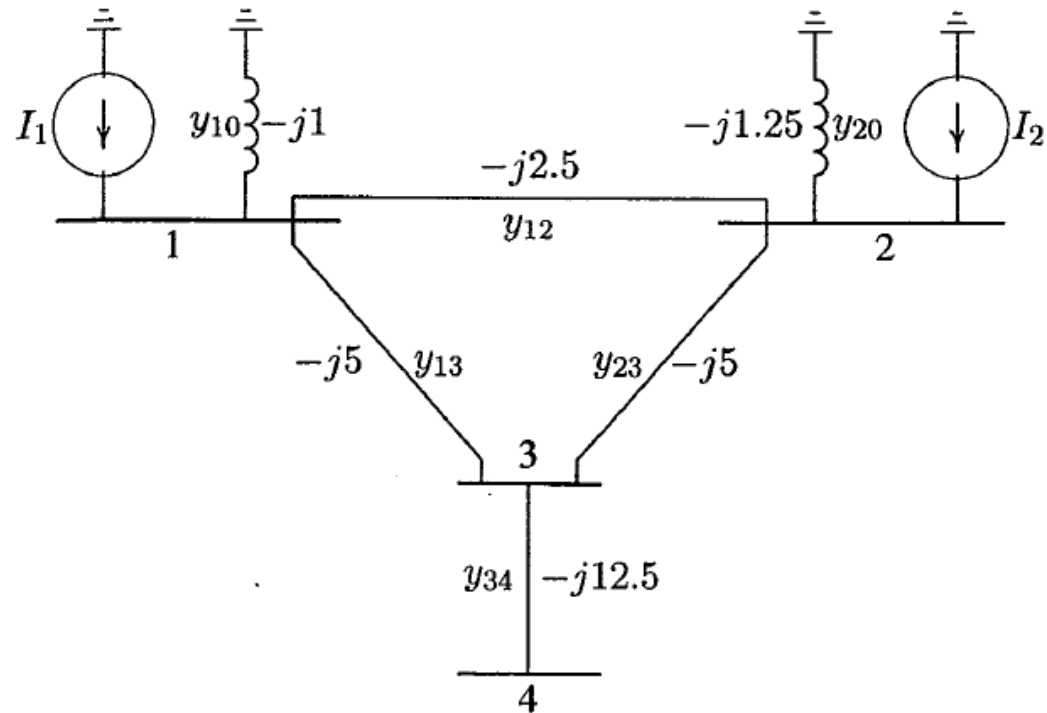


# MATRIK ADMITANSI BUS

$$y_{ij} = \frac{1}{z_{ij}} = \frac{1}{r_{ij} + jx_{ij}}$$



**FIGURE 6.1**  
The impedance diagram of a simple system.



**FIGURE 6.2**  
The admittance diagram for system of Figure 6.1.

$$I_1 = y_{10}V_1 + y_{12}(V_1 - V_2) + y_{13}(V_1 - V_3)$$

$$I_2 = y_{20}V_2 + y_{12}(V_2 - V_1) + y_{23}(V_2 - V_3)$$

$$0 = y_{23}(V_3 - V_2) + y_{13}(V_3 - V_1) + y_{34}(V_3 - V_4)$$

$$0 = y_{34}(V_4 - V_3)$$

$$I_1 = (y_{10} + y_{12} + y_{13})V_1 - y_{12}V_2 - y_{13}V_3$$

$$I_2 = -y_{12}V_1 + (y_{20} + y_{12} + y_{23})V_2 - y_{23}V_3$$

$$0 = -y_{13}V_1 - y_{23}V_2 + (y_{13} + y_{23} + y_{34})V_3 - y_{34}V_4$$

$$0 = -y_{34}V_3 + y_{34}V_4$$

$$Y_{11} = y_{10} + y_{12} + y_{13}$$

$$Y_{22} = y_{20} + y_{12} + y_{23}$$

$$Y_{11} = y_{10} + y_{12} + y_{13}$$

$$Y_{22} = y_{20} + y_{12} + y_{23}$$

$$Y_{33} = y_{13} + y_{23} + y_{34}$$

$$Y_{44} = y_{34}$$

$$Y_{12} = Y_{21} = -y_{12}$$

$$Y_{13} = Y_{31} = -y_{13}$$

$$Y_{23} = Y_{32} = -y_{23}$$

$$Y_{34} = Y_{43} = -y_{34}$$

$$I_1 = Y_{11}V_1 + Y_{12}V_2 + Y_{13}V_3 + Y_{14}V_4$$

$$I_2 = Y_{21}V_1 + Y_{22}V_2 + Y_{23}V_3 + Y_{24}V_4$$

$$I_3 = Y_{31}V_1 + Y_{32}V_2 + Y_{33}V_3 + Y_{34}V_4$$

$$I_4 = Y_{41}V_1 + Y_{42}V_2 + Y_{43}V_3 + Y_{44}V_4$$

$$\begin{bmatrix} I_1 \\ I_2 \\ \vdots \\ I_i \\ \vdots \\ I_n \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} & \cdots & Y_{1i} & \cdots & Y_{1n} \\ Y_{21} & Y_{22} & \cdots & Y_{2i} & \cdots & Y_{2n} \\ \vdots & \vdots & & \vdots & & \vdots \\ Y_{i1} & Y_{i2} & \cdots & Y_{ii} & \cdots & Y_{in} \\ \vdots & \vdots & & \vdots & & \vdots \\ Y_{n1} & Y_{n2} & \cdots & Y_{ni} & \cdots & Y_{nn} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ \vdots \\ V_i \\ \vdots \\ V_n \end{bmatrix}$$

$$\mathbf{I}_{bus} = \mathbf{Y}_{bus} \mathbf{V}_{bus}$$

$$\mathbf{V}_{bus} = \mathbf{Y}_{bus}^{-1} \mathbf{I}_{bus}$$

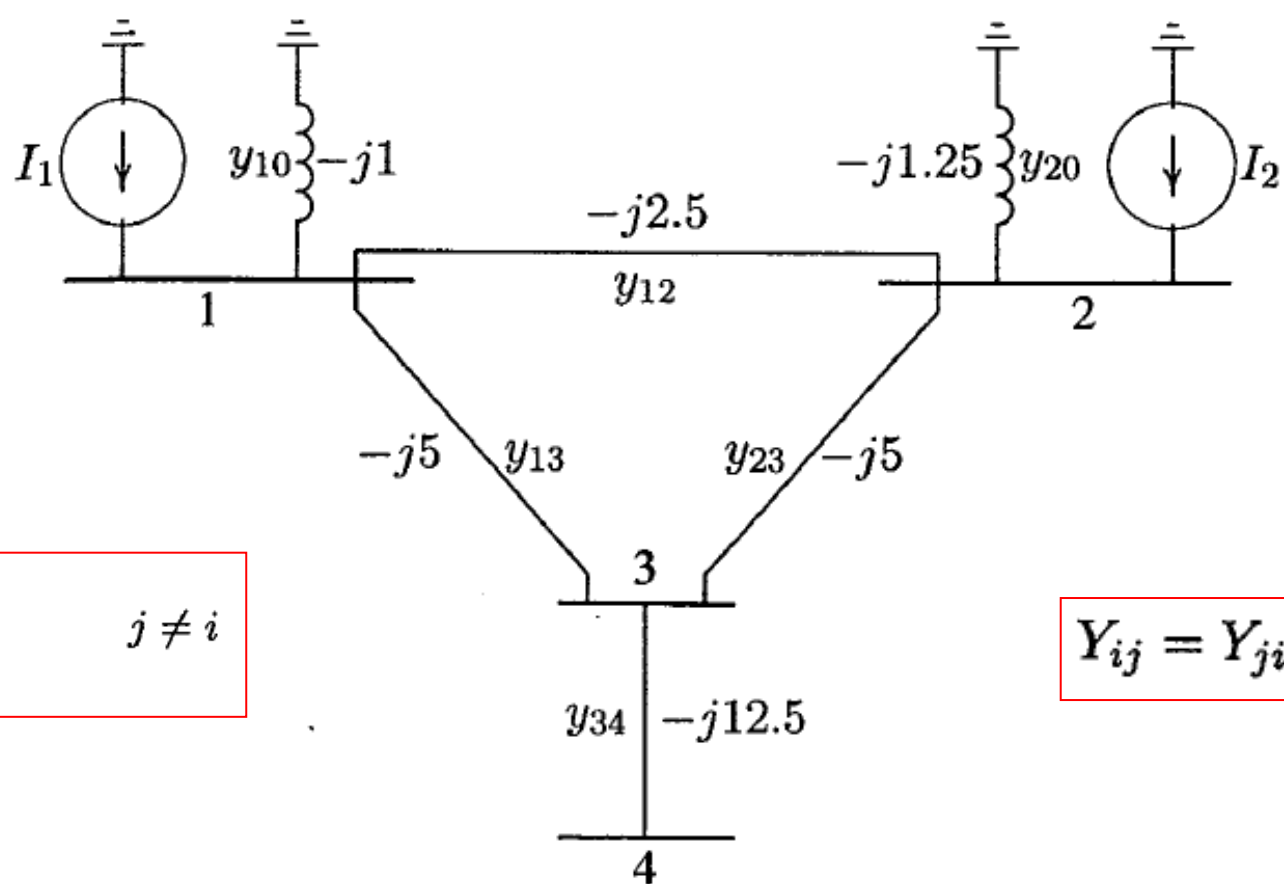
*self-admittance or driving point admittance,*

$$Y_{ii} = \sum_{j=0}^n y_{ij}$$

$$j \neq i$$

*mutual admittance or transfer admittance,*

$$Y_{ij} = Y_{ji} = -y_{ij}$$



$$Y_{ii} = \sum_{j=0}^n y_{ij} \quad j \neq i$$

$$Y_{ij} = Y_{ji} = -y_{ij}$$

**FIGURE 6.2**

The admittance diagram for system of Figure 6.1.

$$Y_{bus} = \begin{bmatrix} -j8.50 & j2.50 & j5.00 & 0 \\ j2.50 & -j8.75 & j5.00 & 0 \\ j5.00 & j5.00 & -j22.50 & j12.50 \\ 0 & 0 & j12.50 & -j12.50 \end{bmatrix}$$

# TUGAS

6.1. A power system network is shown in Figure 6.17. The generators at buses 1 and 2 are represented by their equivalent current sources with their reactances in per unit on a 100-MVA base. The lines are represented by  $\pi$  model where series reactances and shunt reactances are also expressed in per unit on a 100 MVA base. The loads at buses 3 and 4 are expressed in MW and Mvar.

(a) Assuming a voltage magnitude of 1.0 per unit at buses 3 and 4, convert the loads to per unit impedances. Convert network impedances to admittances and obtain the bus admittance matrix by inspection.

(b) Use the function  $\mathbf{Y} = \mathbf{ybus}(\mathbf{zdata})$  to obtain the bus admittance matrix. The function argument  $\mathbf{zdata}$  is a matrix containing the line bus numbers, resistance and reactance. (See Example 6.1.)

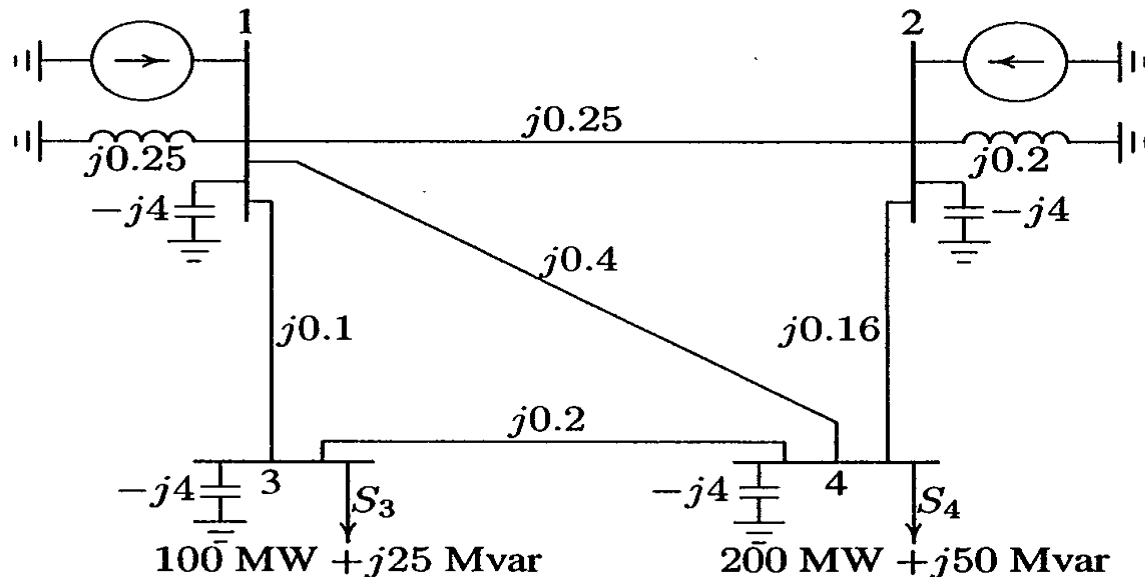


FIGURE 6.17

One-line diagram for Problem 6.1.